

The embodiments of the invention in which an exclusive property or privilege is claimed are defined as follows:

1. A controlled offset amplifier, comprising:
 - (a) an input stage including two transistors, said two transistors having different threshold voltage implants; and
 - (b) an amplification stage that receives a signal from said input stage and provides an output signal related to said signal.
2. The amplifier of Claim 1, wherein one of said two transistors does not have a threshold voltage implant.
3. The amplifier of Claim 2, wherein said input stage comprises two source coupled transistors.
4. The amplifier of Claim 1, wherein said transistors are p-channel MOS transistors.
5. The amplifier of Claim 2, wherein said transistors are p-channel MOS transistors.
6. A method for forming a controlled-offset amplifier comprising:
 - (a) forming an amplifier stage;
 - (b) forming an input stage comprised of two input transistors; and
 - (c) applying a threshold voltage implant to only one of said input transistors.
7. The method of Claim 6, wherein said transistors are p-channel MOS transistors.